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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/782,474	02/19/2004	Vivek G. Gupta	MP1438 130197	4905
64331 7590 07/16/2007 OLIFF & BERRIDGE, PLC P.O. BOX 19928 ALEXANDRIA, VA 22320			EXAMINER TRINH, TAN H	
			ART UNIT 2618	PAPER NUMBER
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Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary

Application No.

10/782,474

Applicant(s)

GUPTA, VIVEK G.

Examiner

TAN TRINH

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 19 February 2004.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-27 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-5, 7-10, 14-20 and 22-27 is/are rejected.
- 7) ☒ Claim(s) 6, 11-13 and 21 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 19 February 2004 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Rejections - 35 USC § 103

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

2. Claims 1-5, 7-10, 14-20 and 22-27 are rejected under 35 U.S.C. 103(a) as being unpatentable over Stengel (U.S. Patent No. 7050763) in view of Clark (U.S. Patent No. 5425022).

Regarding claims 1, 9, 14, Stengel teaches a method, including: reserving access for a source device (10) (see fig. 1), included in a plurality N of source devices (see fig. 3, source devices 50A-C and 60A-B, col. 9, lines 57-62) to allocated channels accessible by a set of target devices (see fig. 4, Sink devices 30, 40, 70 and 80 on the electronic units 3, 4, 7 and 8, col. 9, lines 63-67) included in the plurality of source devices (see fig. 3, source devices 50A-C and 60A-B, col. 9, lines 57-62). But Stengel does not mention the creating a static map, wherein N is a positive integer. N-1 logical, and logical channels.

However, Clark teaches the creating a static map, wherein N is a positive integer. N-1 logical and logical channels (see fig. 6 and 9, configuration Map for device 5, byte, logical channels with N (#) channel and device, col. 4, lines 5-25 and lines 42-49).

Therefore, it would have been obvious to one of ordinary skill in the art at the time invention was made to modify above teaching of Stengel with Clark, in order to reduces the delay between incoming and outgoing frames (see suggested by Clark on col. 4, lines 45-49).

Regarding claim 19, Stengel teaches a system (see fig. 1 and 6), including: a plurality N of source devices having (see fig. 3, source devices 50A-C and 60A-B, col. 9, lines 57-62), access to allocated channels accessible by a set of target devices (see fig. 4, Sink devices 30, 40, 70 and 80 on the electronic units 3, 4, 7 and 8, col. 9, lines 63-67) included in the plurality of source devices (see fig. 3, source devices 50A-C and 60A-B, col. 9, lines 57-62); and an omnidirectional antenna coupled to at least one of the plurality of source devices (see figs. 1-5 with antennas, col. 2, lines 61-67, and col. 11, lines 33-40). In this case, the omnidirectional antennas are the whip antenna, a vertically orientated dipole antenna, the discone antenna, and the horizontal loop (or halo) antenna. But Stengel does not mention a static map,

However, Clark teaches the creating a static map, (see fig. 6 and 9, configuration Map for device 5, byte, logical channels with N (#) channel and device, col. 4, lines 5-25 and lines 42-49).

Therefore, it would have been obvious to one of ordinary skill in the art at the time invention was made to modify above teaching of Stengel with Clark, in order to reduces the delay between incoming and outgoing frames (see suggested by Clark on col. 4, lines 45-49).

Regarding claims 25, Stengel teaches an apparatus (100) (see fig. 1, system 100), including: a source device included in a plurality N of source devices (see fig. 3, source devices 50A-C and 60A-B, col. 9, lines 57-62) having access to allocated channels accessible by a set of target devices included in the plurality of source devices (see fig. 4, Sink devices 30, 40, 70 and 80 on the electronic units 3, 4, 7 and 8, col. 9, lines 63-67), and a memory to store the static map

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(see (see col. 5, lines 29-40); wherein the source device is selected from one of an applications subsystem and a communications subsystem (see fig. 1, source device 10 is selected from the subsystem 100) . But Stengel does not mention the creating a static map, wherein N is a positive integer. N-1 logical, and logical channels.

However, Clark teaches the creating a static map, wherein N is a positive integer. N-1 logical and logical channels (see fig. 6 and 9, configuration Map for device 5, byte, logical channels with N (#) channel and device, col. 4, lines 5-25 and lines 42-49).

Therefore, it would have been obvious to one of ordinary skill in the art at the time invention was made to modify above teaching of Stengel with Clark, in order to reduces the delay between incoming and outgoing frames (see suggested by Clark on col. 4, lines 45-49).

Regarding claims 2, 10 and 17, Clark teaches further including: storing at least a portion of the static map in a memory (see col. 5, lines 29-40).

Regarding claims 3 and 15, Clark teaches further including: changing the static map responsive to an indication received *from one of* an entity selected from the source device, a device controller (see col. 4, lines 14-25).

Regarding claims 4, 16 and 20, Clark teaches sending a message having an indication of the N-1 logical channels from the source device to at least one of the target devices included in the set of target devices (see fig. 9, col. 4, lines 17-28). In this case, the source device transmits

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the logical channel with table lookup and target devices detect the signal and response to the received signals and read the data transmitted from source device.

Regarding claim 5, Clark teaches further including: designating the identity of the source device within the plurality of source devices using an arbitration scheme (see col. 5, lines 29-52 with device ID).

Regarding claim 7, Stengel teaches requesting allocation of a channel from the source device by a target device included in the set of target devices; and granting the allocation of the channel to the target device by the source device (see col. 3, lines 55-67, and col. 6, lines 26-30).

Regarding claim 8, Stengel teaches further including: booting the source device after the reserving (see col. 10, lines 52-col. 11, lines 7). In this case, the bootstrap circuit is connected between power supply Vcc and supply terminal of the circuit DRV1 and giving high-side of T1 for purposes of switching and booting the source device.

Regarding claim 18, Stengel teaches the source device is selected from one of a desktop computer, a laptop computer, a cellular telephone, a device capable of communicating over a wireless local area network (WLAN), and a communications subsystem (see col. 7, lines 25-44, and col. 8, lines 23-33 for computer and CPU).

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Regarding claim 22, Stengel teaches further including: a transceiver included in at least one of the target devices (see fig. 3, TX 51 and RX 52 and see fig. 4, Sink devices 30, 40, 70 and 80 on the electronic units 3, 4, 7 and 8, col. 9, lines 63-67), and an energy conduit to couple at least one of the source devices to at least one of the target devices (see fig. 3, source devices 50A-C and 60A-B, col. 9, lines 57-62, and see fig. 1, TX 11 with antenna transmit signal to RX 21 antenna by the logical channel, col. 3, lines 55-67. or see col. 1, lines 35-37 with electrical conductive connection). In this case, the energy transmits from source device to target device by the communication channel, that is the energy conduit to couple at source device and target device.

Regarding claims 23 and 26, Stengel teaches the energy conduit comprises a multi-drop link (see fig. 5, chip1 to chip2 and multi-drop link for chip 3 and 4).

Regarding claims 24 and 27, Stengel teaches the plurality of source devices N are included in a single physical device (see fig. 1, system 100 is a single physical device).

Allowable Subject Matter

3. Claims 6, 11-13 and 21 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Reasons for allowance

4. The following is an examiner's statement of reasons for allowance:

Regarding dependent claim 6, Stengel or Clark fail to disclose; the method of claim 1, further including: setting a channel by a target device included in the set of target devices; and clearing the channel by the target device as specified in dependent claim 6.

Regarding dependent claim 11, Stengel or Clark fail to disclose when accessed by the machine, results in the machine performing: determining a need for a channel by a target device included in the set of target devices; and setting the channel by the target device as specified in dependent claim 11.

Regarding dependent claim 21, Stengel or Clark fail to disclose the channel map is to map a second subset of the N-1 logical channels not including the first subset of logical channels to a set of channels accessible to a second target device included in the set of target devices as specified in dependent claim 21.

Conclusion

5. **Any response to this action should be mailed to:**

Commissioner of Patents and Trademarks
Washington, D.C. 20231

or faxed to:

(571) 273-8300, (for Technology Center 2600 only)

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*Hand-delivered responses should be brought to the Customer Service Window (now located at the **Randolph Building, 401 Dulany Street, Alexandria, VA 22314**).*

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tan Trinh whose telephone number is (571) 272-7888. The examiner can normally be reached on Monday-Friday from 9:30 AM to 6:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiners supervisor, Anderson, Matthew D., can be reached at (571) 272-4177.

The fax phone number for the organization where this application or proceeding is assigned is (571) 273-8300.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the **Technology Center 2600 Customer Service Office** whose telephone number is (703) 306-0377.

7. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Tan H. Trinh
Division 2618
July 6, 2007

PATENT EXAMINER
TRINH, TAN

